



P026

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Drew D. Perkins et al.

Group Art Unit: 2633

Serial No.: 10/712,732

Examiner: None Yet Assigned

Filed: November 12, 2003

Confirmation No. 9275

Title: **METHOD AND APPARATUS FOR DEPLOYING FORWARD
ERROR CORRECTION IN OPTICAL TRANSMISSION
NETWORKS AND THE DEPLOYMENT OF PHOTONIC
INTEGRATED CIRCUIT (PIC) CHIPS WITH THE SAME**

INFORMATION DISCLOSURE STATEMENT

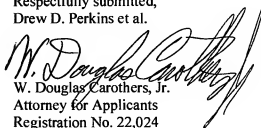
Commissioner for Patents
PO Box 1450
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Sir:

Applicants submit herewith documents of which they are aware, which they believe may be material to the examination of this application and in respect to a duty of disclosure in accordance with 37 CFR 1.56. A copy of each reference is enclosed. These references were reviewed and considered in the preparation of this patent application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this Statement, and no representation is being made that a search has been conducted or that this Statement encompasses all the possible relevant information.

The Applicant believes that no fee is required for submission of this Statement because it is being submitted before the mailing date of the first Office Action on the merits in accordance with 37 CFR 1.97(b)(3). However, if a fee is required, the undersigned authorizes the Commissioner to charge the necessary fee, or credit any overpayment, to USPTO Deposit Account No. 502022.

Respectfully submitted,
Drew D. Perkins et al.



W. Douglas Carothers, Jr.
Attorney for Applicants
Registration No. 22,024
Tel. No. (408) 572-5340
Fax No. (408) 572-5343
Date: February 16, 2004
Sunnyvale, California

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I hereby certify that this correspondence together with the above mentioned attachments are being deposited on the date below with the United States Postal Service as first class mail in an envelope addressed to:

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